**DAILY ASSESSMENT FORMAT**

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| **Date:** | **05 August 2020** | **Name:** | **Pragati M Kundalkar** |
| **Course:** | **Coursera** | **USN:** | **4AL17EC072** |
| **Topic:** | **VLSI CAD part1 : logic** | **Semester & Section:** | **6th sem ‘B’ sec** |
| **Github Repository:** | **Pragati-m-k** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |
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